# Technetics Semiconductor Engineering Center



The dedicated Technetics Semiconductor Engineering Center, located in the heart of Silicon Valley, offers custom solutions and innovative technologies for the most critical of applications in the semiconductor industry. With focus on new product integration and technology development programs, the Technetics Semiconductor Engineering Center offers a number of capabilities that support your project needs and meet the highest of industry standards.

# **INCUBATOR/PROJECT SPACE**

- 4896 ft² Facility Space
- 1200 ft<sup>2</sup> Class 1000 Clean Room
- Gray Area Work Cells
- Power/CDA/Vacuum/DI Water

## **PROCESS DEVELOPMENT**

- Refurbishment Programs
- NPI/Pilot/Proof of Concept

## **ESC CAPABILITIES**

- Flatness
- Surface Roughness
- Dielectric
- Dimensional
- Polishing

# **CHAMBER CAPABILITIES**

- Process Gases/Abatement System
- Plasma Processing
- Vacuum Chamber Configurable (200mm/300mm)
- Thermal Mapping IR/TC Wafer

## **BONDING**

- Ceramic
- Metals
- Plastics

## **PROTOTYPE ASSEMBLIES**

- Electro Mechanical
- Vacuum Assemblies
- Vertical Integration

#### **INSPECTION**

- Borescope
- Potential Mapping
- CMN/
- Current Source/Leakage Current

## **ORBITAL WELDING**

- Gas Distribution
- Fluidics



Class 1000 Clean Room

